<u>TOSHIBA</u>

Note: xxxFN (JEDEC SOP) is not available in

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HC139AP,TC74HC139AF,TC74HC139AFN

Dual 2-to-4 Line Decoder

The TC74HC139A is a high speed CMOS 2-to-4 LINE DECODER/DEMULTIPLEXER fabricated with silicon gate C²MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

The active low enable input can be used for gating or it can be used as a data input for demultiplexing applications.

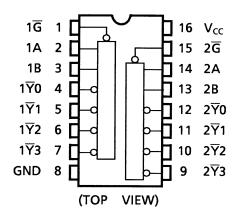
When the enable input is held "H", all four outputs are fixed at a high logic level independent of the other inputs.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

- High speed: $t_{pd} = 16 \text{ ns}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: I_{CC} = 4 μA (max) at Ta = 25°C
- High noise immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (min)
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: |IOH| = IOL = 4 mA (min)
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: V_{CC} (opr) = 2~6 V
- Pin and function compatible with 74LS139

Pin Assignment



 Japan.

 TC74HC139AP

 IP16-P-300-2.54A

 TC74HC139AF

 IP16-P-300-2.54A

 TC74HC139AF

 SOP16-P-300-1.27A

 TC74HC139AFN

 IP16-P-300-1.27A

 SOP16-P-300-1.27A

 SOP16-P-300-1.27A

 SOL16-P-150-1.27

 Weight

 DIP16-P-300-2.54A

 SOP16-P-300-1.27A

 SOL16-P-150-1.27

 SOL16-P-150-1.27

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IEC Logic Symbol

1A <u>(2)</u> 1B <u>(3)</u> 1 G (1) N	1 2 EN	X/Y	0 1 2 3	$(4) 1\overline{Y}_{0}$ $(5) 1\overline{Y}_{1}$ $(6) 1\overline{Y}_{2}$ $(7) 1\overline{Y}_{3}$
2A <u>(14)</u> 2B <u>(13)</u> 2G <u>(15)</u> N				$(12) 2\overline{Y}_0$ $(11) 2\overline{Y}_1$ $(10) 2\overline{Y}_2$ $(9) 2\overline{Y}_3$

1A (2) 1B (3) 1 G (1) N	$\begin{array}{c} DMUX \\ 0 \\ 1 \end{array} G \frac{0}{3} \end{array}$	0 1 2 3	$(4) 1\overline{Y}0$ $(5) 1\overline{Y}1$ $(6) 1\overline{Y}2$ $(7) 1\overline{Y}3$
2A <u>(14)</u> 2B <u>(13)</u> 2G <u>(15)</u> N			$(12) 2\overline{Y}_0$ $(11) 2\overline{Y}_1$ $(10) 2\overline{Y}_2$ $(9) 2\overline{Y}_3$

Truth Table

I	nputs						
Enable	Enable Select		Ϋ́0	T1	Ϋ́2	¥3	Selected Output
G	В	А	TU	TI	12	15	•
Н	Х	Х	Н	Н	Н	Н	None
L	L	L	L	Н	Н	Н	Υ 0
L	L	Н	Н	L	Н	Н	Ϋ́1
L	Н	L	Н	Н	L	Н	۲ 2
L	Н	Н	Н	Н	Н	L	¥3

X: Don't care

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5~7	V
DC input voltage	V _{IN}	-0.5~V _{CC} + 0.5	V
DC output voltage	V _{OUT}	$-0.5 \sim V_{CC} + 0.5$	V
Input diode current	I _{IK}	±20	mA
Output diode current	IOK	±20	mA
DC output current	IOUT	±25	mA
DC V _{CC} /ground current	ICC	±50	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	-65~150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of $Ta = -40^{\circ}C \sim 65^{\circ}C$. From $Ta = 65^{\circ}C$ to $85^{\circ}C$ a derating factor of -10 mW/°C shall be applied until 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2~6	V
Input voltage	V _{IN}	0~V _{CC}	V
Output voltage	V _{OUT}	0~V _{CC}	V
Operating temperature	T _{opr}	-40~85	°C
		0~1000 (V _{CC} = 2.0 V)	
Input rise and fall time	t _r , t _f	0~500 (V _{CC} = 4.5 V)	ns
		0~400 (V _{CC} = 6.0 V)	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

			Test Condition		-	Ta = 25°0	a = 25°C		Ta = −40~85°C	
Characteristics	Symbol				Min	Тур.	Max	Min	Max	Unit
				2.0	1.50	_	_	1.50	_	
High-level input voltage	VIH			4.5	3.15			3.15	—	V
Ũ				6.0	4.20		_	4.20	—	
				2.0			0.50		0.50	
Low-level input voltage	VIL		_	4.5			1.35		1.35	V
Ũ				6.0			1.80		1.80	
	Vон	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -20 μA	2.0	1.9	2.0		1.9	—	
				4.5	4.4	4.5		4.4	—	
High-level output voltage				6.0	5.9	6.0	—	5.9	—	V
Ũ			$I_{OH} = -4 \text{ mA}$	4.5	4.18	4.31	_	4.13	_	
			I _{OH} = -5.2 mA	6.0	5.68	5.80	—	5.63	—	
		VIN		2.0	_	0.0	0.1	_	0.1	
			$I_{OL} = 20 \ \mu A$	4.5	—	0.0	0.1		0.1	
Low-level output voltage	V _{OL}	= V _{IH} or		6.0	—	0.0	0.1		0.1	V
5		VIL	I _{OL} = 4 mA	4.5	_	0.17	0.26	_	0.33	
			I _{OL} = 5.2 mA	= 5.2 mA 6.0		0.18	0.26		0.33	
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		6.0	_		±0.1	_	±1.0	μΑ
Quiescent supply current	ICC	$V_{IN} = V_C$	_C or GND	6.0	_	_	4.0	_	40.0	μΑ

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AC Characteristics ($C_L = 15 \text{ pF}$, $V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}C$, input: $t_r = t_f = 6 \text{ ns}$)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	t _{TLH}			4	8	ns
	t _{THL}		_	-1	0	115
Propagation delay time	t _{pLH}			12	22	20
(A, B- Y)	t _{pHL}	—	_	12	22	ns
Propagation delay time	t _{pLH}			10	18	20
$(\overline{G} - \overline{Y})$	t _{pHL}			10	10	ns

AC Characteristics ($C_L = 50 \text{ pF}$, input: $t_r = t_f = 6 \text{ ns}$)

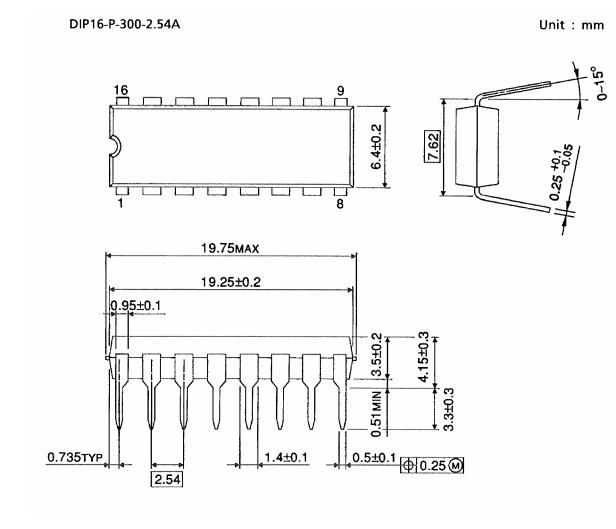
		Test Condition			Ta = 25°C)	Ta = -4		
Characteristics	Characteristics Symbol		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
	4		2.0	_	30	75	_	95	
Output transition time	t _{TLH}	—	4.5	—	8	15	—	19	ns
	tthl		6.0	—	7	13	—	16	
Propagation delay			2.0		45	130		165	
time	t _{pLH}	_	4.5	_	15	26		33	ns
(A, B- <u>Y</u>)	t _{pHL}		6.0		13	22	—	28	
Propagation delay			2.0		39	110		140	
time	t _{pLH}	_	4.5	_	13	22		28	ns
$(\overline{G} - \overline{Y})$	t _{pHL}		6.0		11	19		24	
Input capacitance	C _{IN}	_	•		5	10		10	pF
Power dissipation capacitance	C _{PD} (Note)	_		_	46	_	_	_	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 I_{CC} (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$ (per decoder)

Package Dimensions



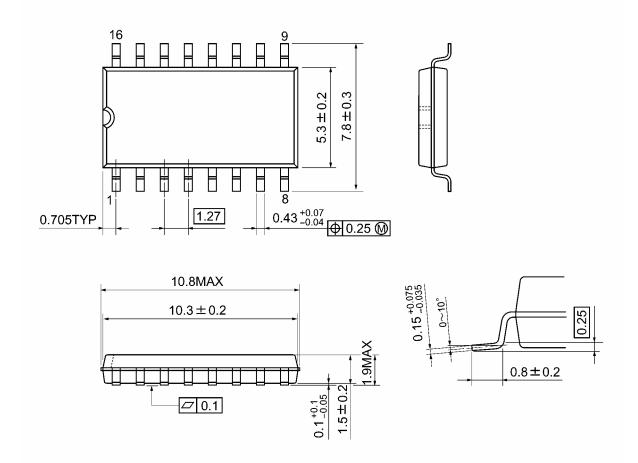
Weight: 1.00 g (typ.)



Package Dimensions

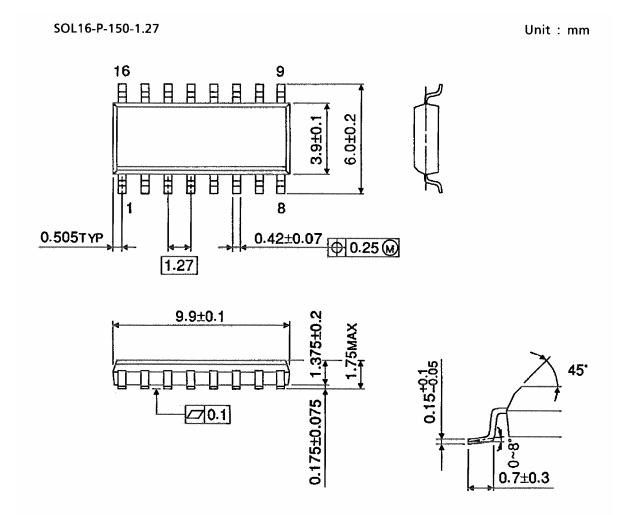
SOP16-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

Package Dimensions (Note)



Note: This package is not available in Japan.

Weight: 0.13 g (typ.)

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20070701-EN GENERAL

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